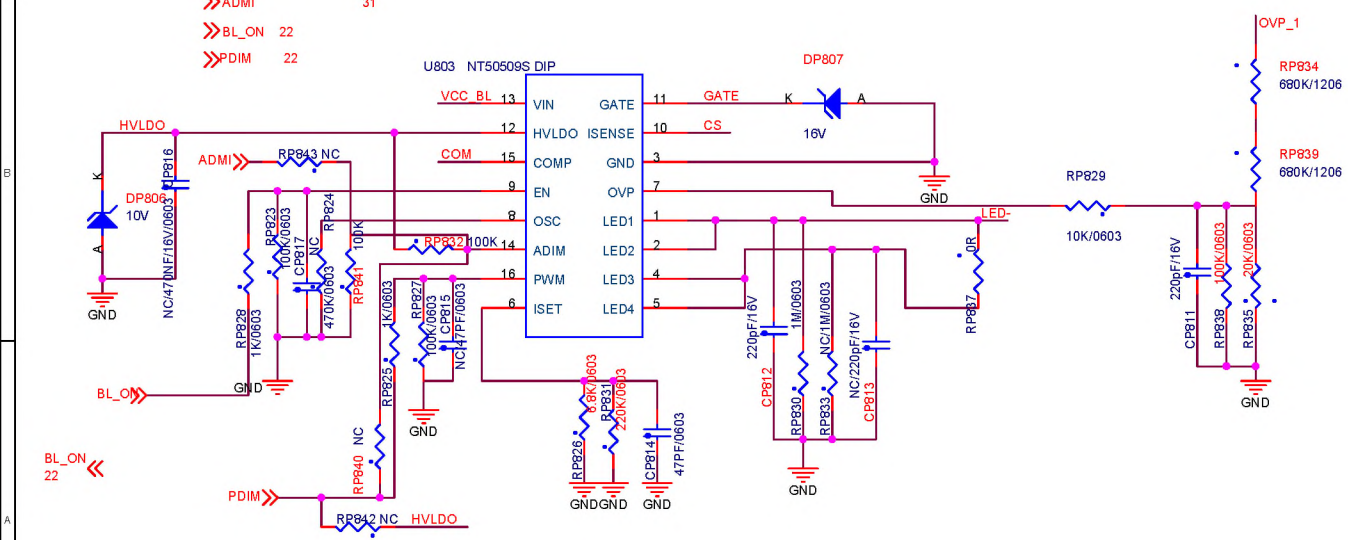


>>ADMI 31
 >>BL_ON 22
 >>PDIM 22



PF7710:
 U802, RP812, RP813, RP814, RP819
 CP806, RP810, CP805, DP805, RP820
 RP821

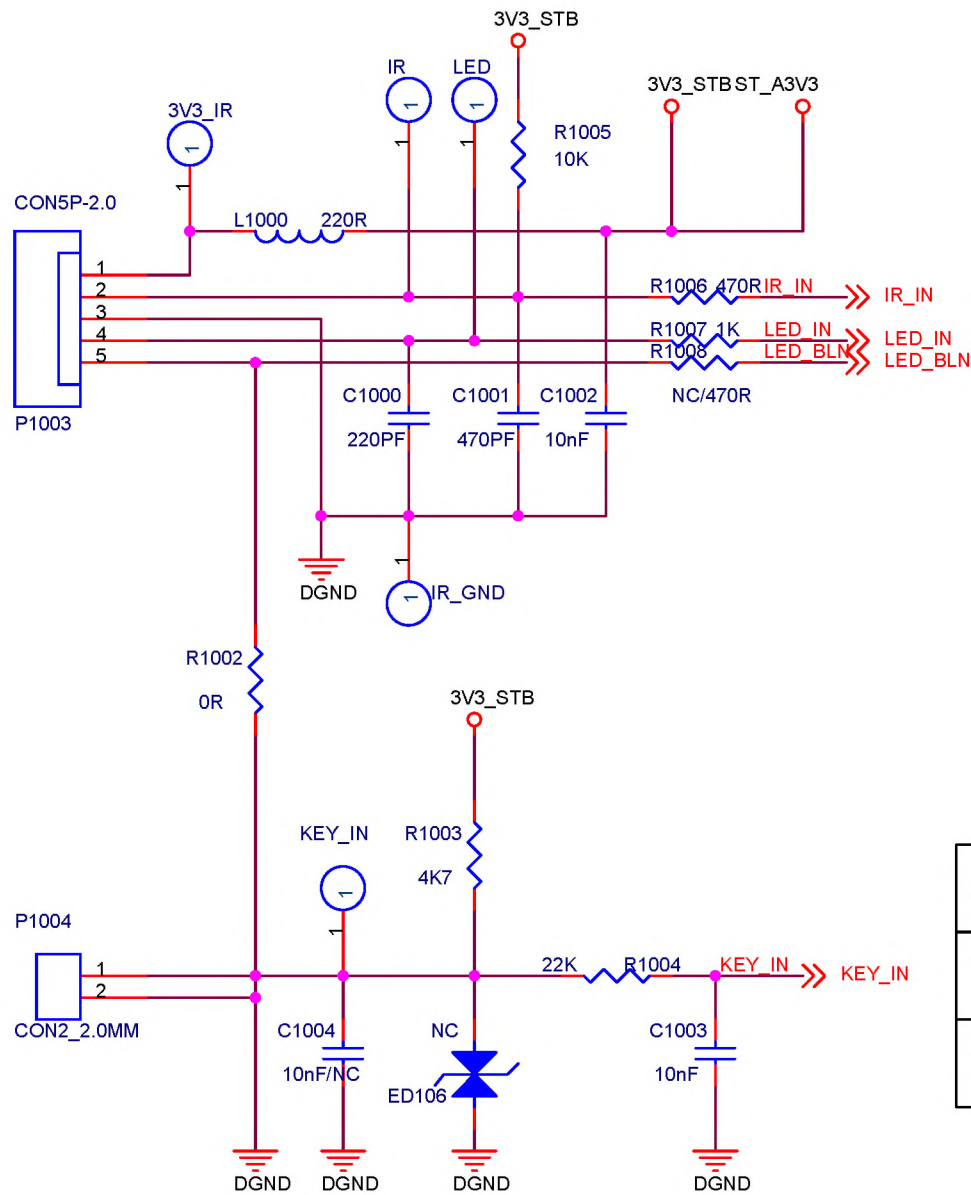
NT50509S:
 U803, DP806, RP823, RP828, RP835, RP829
 RP824, RP825, RP827, RP826, RP838
 RP831, CP814, RP820, RP821, CP811, DP807

Title		<Title>
Size	Document Number	Rev
B	BACKLIGHT	<RevCode>
Date:	Sunday, January 20, 2019	Sheet 1 of 1

共用: LH901, DP801, RP801, RP802, CP802, FB803, EC801, EC802, CP801, CN802, DP804, QP801, RP803, RP804, RP805, RP806, RP807, CP804, CP808, CP809, RP817, CP812

From Main Chip

10 KEYPAD_1 ← KEYPAD_1



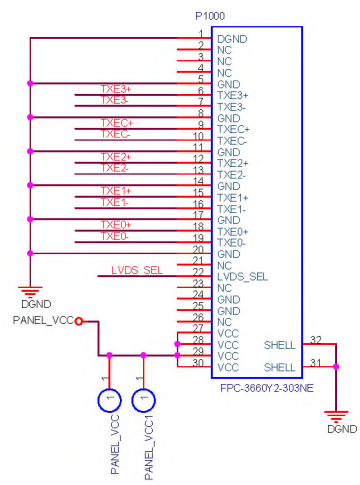
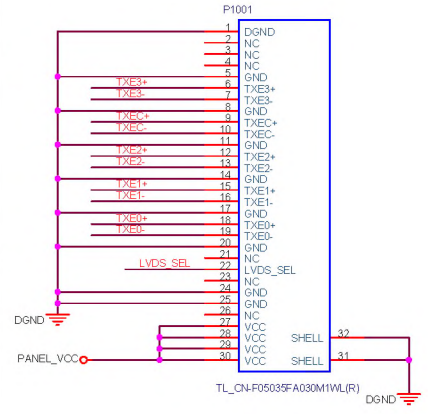
Note:	P1003	P1004	R1002	R1008	LED_BLN
Key&IR 2IN1	ON	NC	ON	NC	N.A
Separate	ON	ON	NC	NC	N.A
	ON	ON	NC	ON	Separate

Title			RTD2841PFG LVDS		
Size	Schematic Name		Rev		
A	Key/LED/IrDA		T1		
Date			Sheet		
Sunday, January 20, 2019			09 of 12		

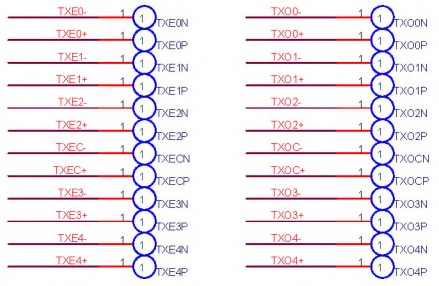
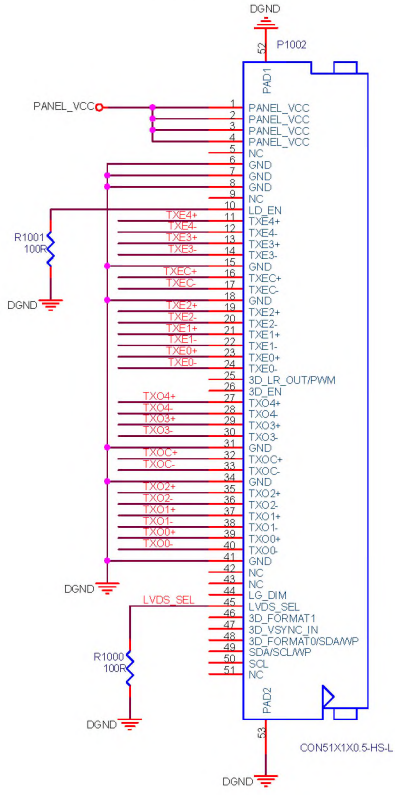
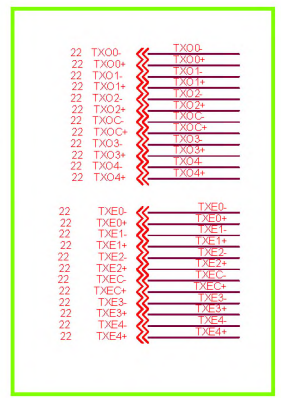
IC->LVDS

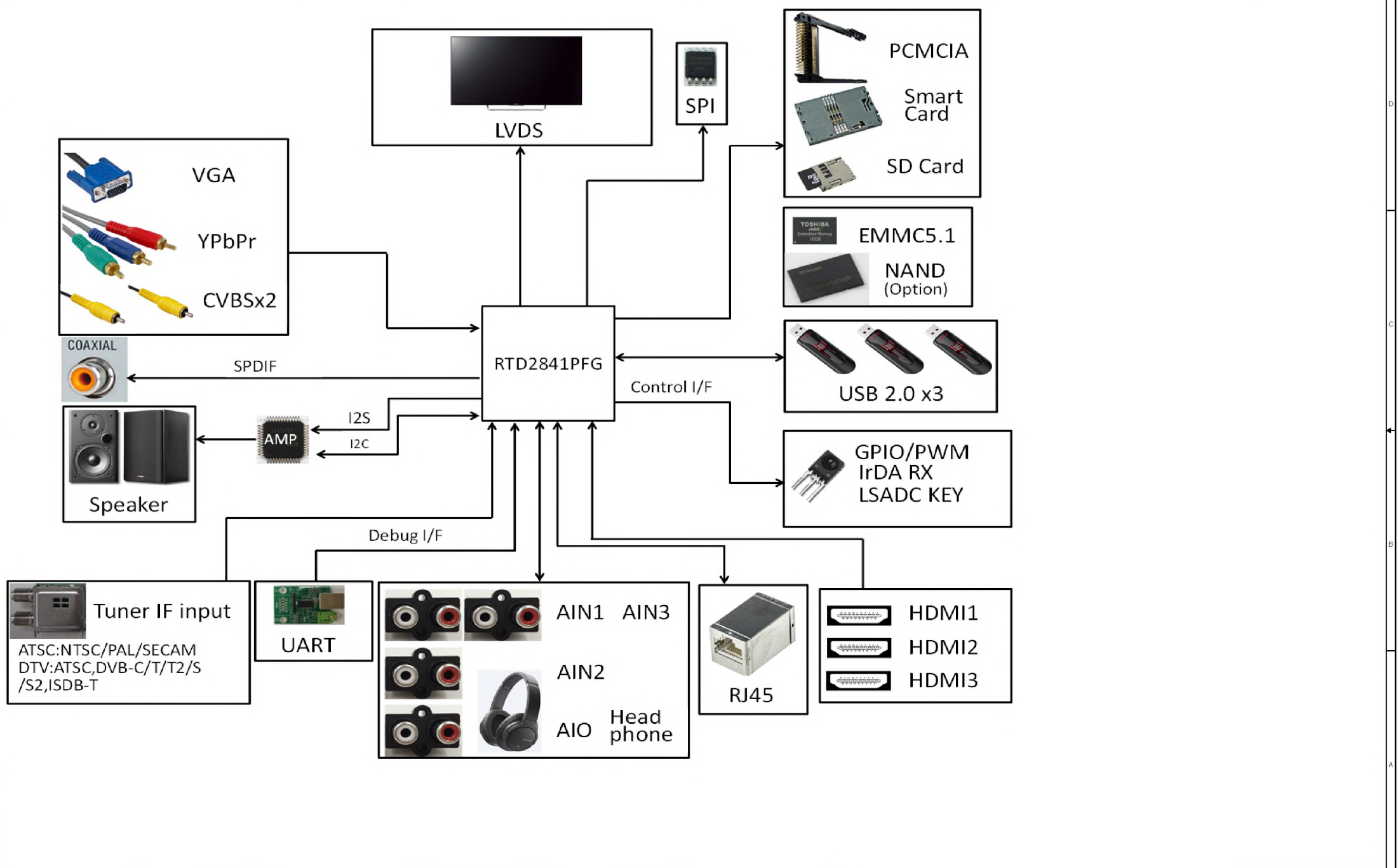
0.5MM_HD 30P FFC

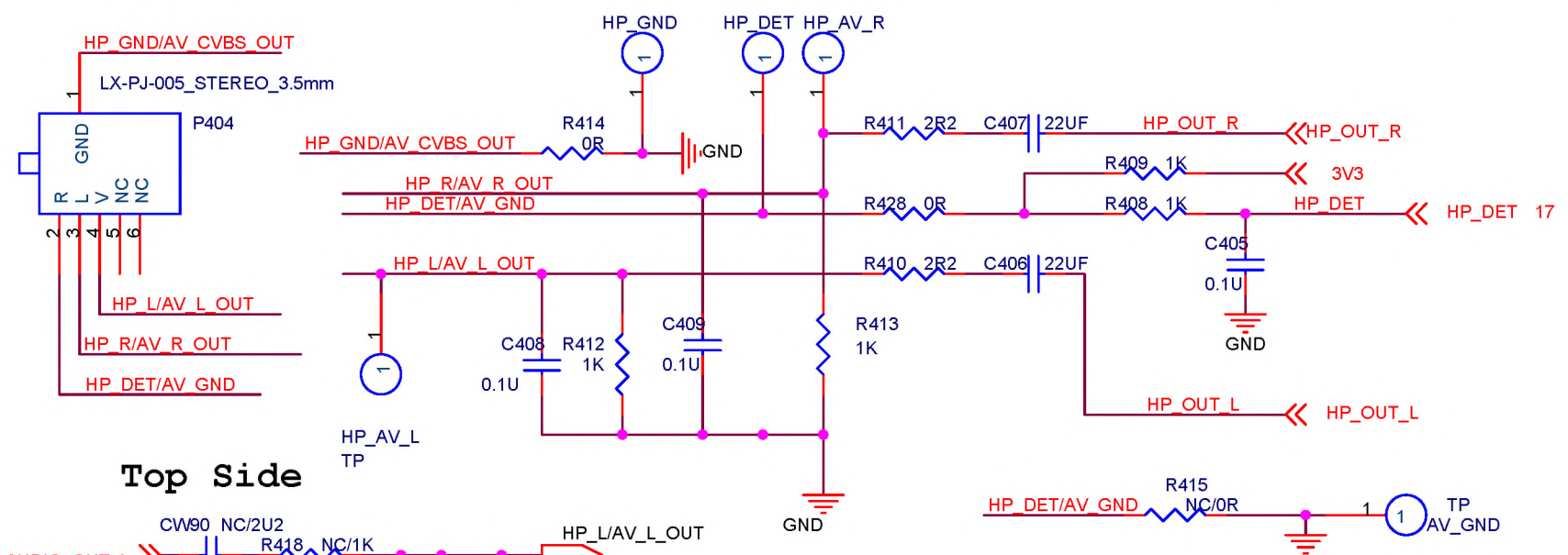
1.0MM_HD 30P FFC



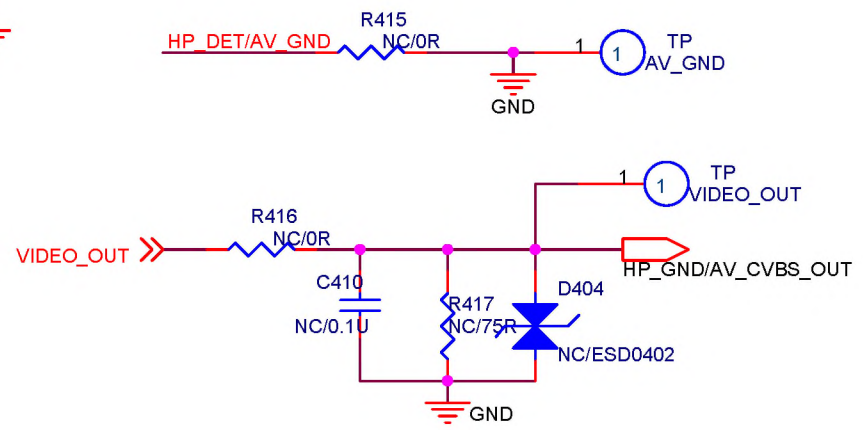
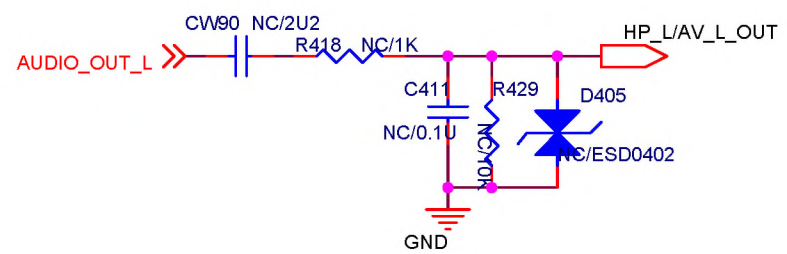
0.5MM_FHD 51P



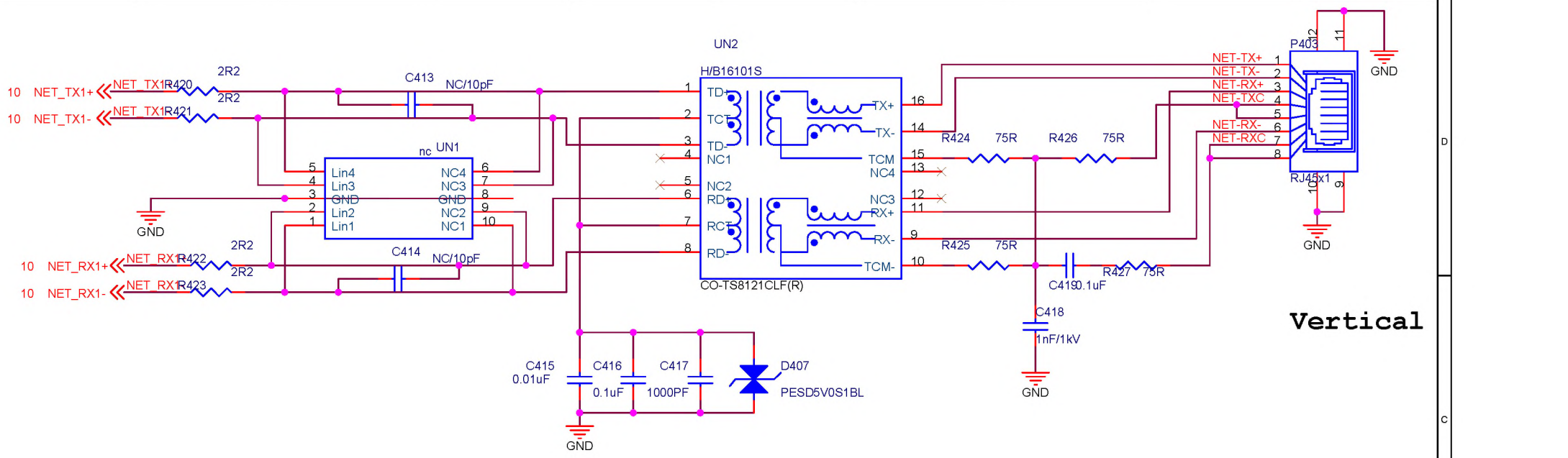




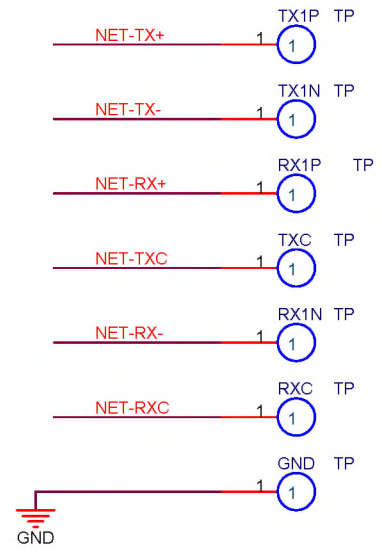
Top Side

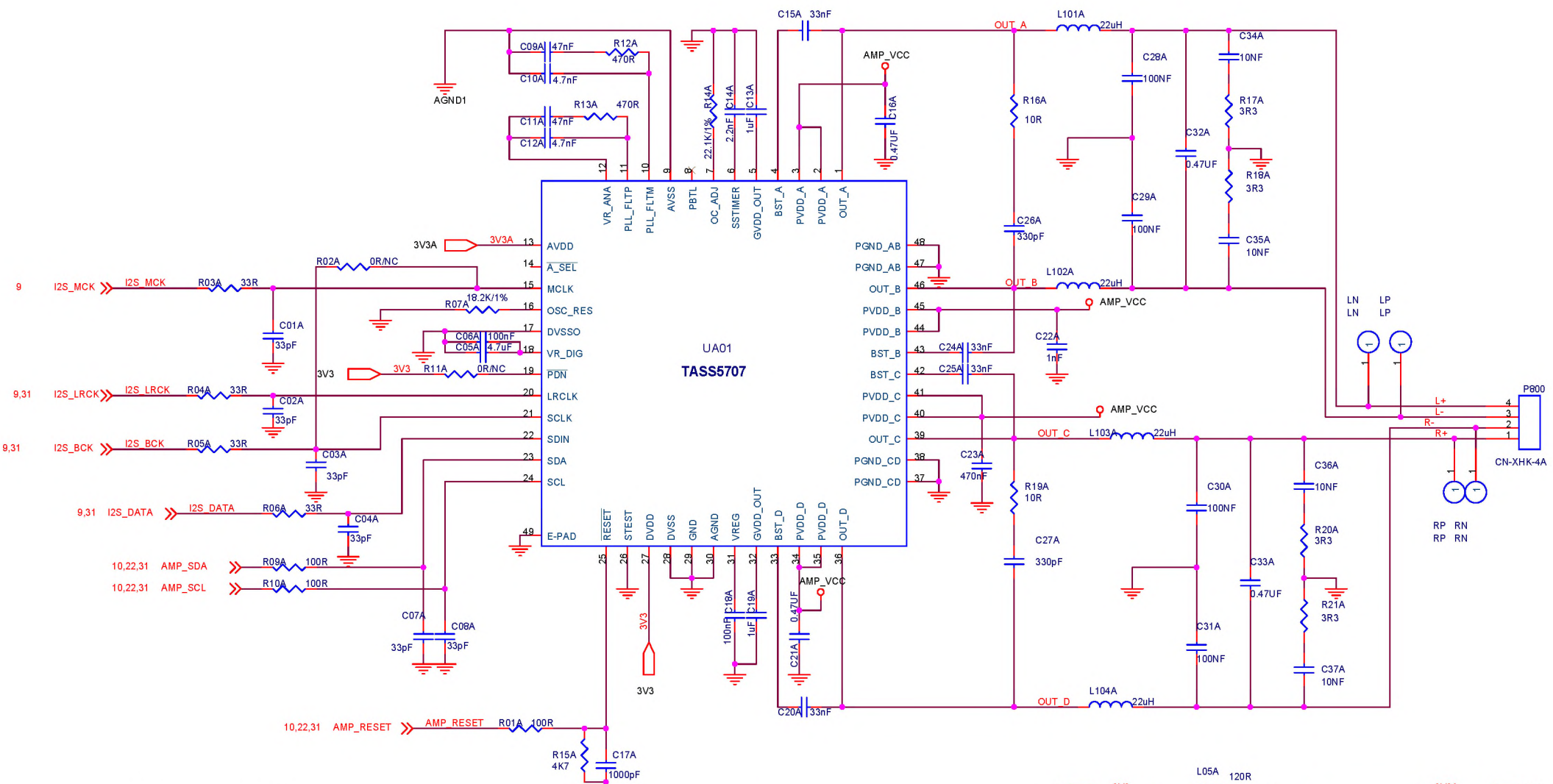


	Headhone ●	AV Out ●	Line Out ●
P1	HP_GND	VIDEO_OUT	NC
P2	HP_DET	AV_GND	AV_GND
P3	HP_R	AV_R_OUT	LINE_OUT_R
P4	HP_L	AV_L_OUT	LINE_OUT_L
R414	0R	NC	NC
R415	NC	0R	0R
R428	0R	NC	NC

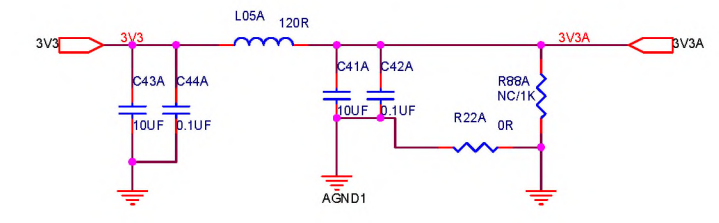
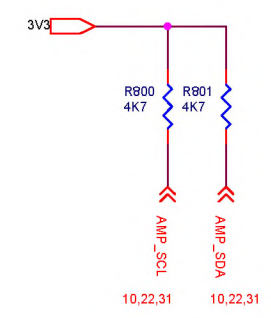
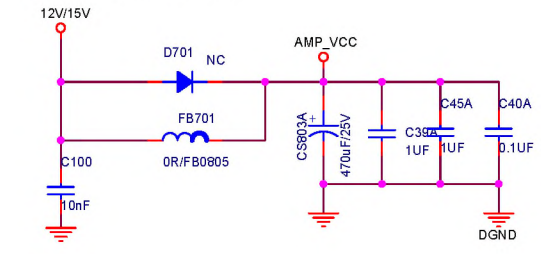


Vertical

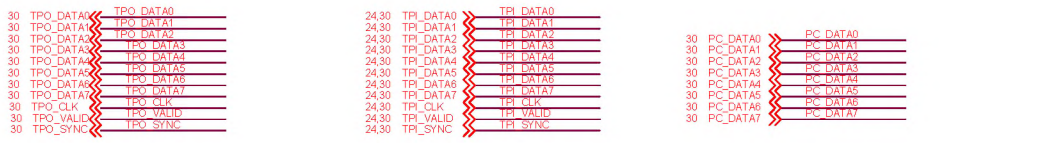
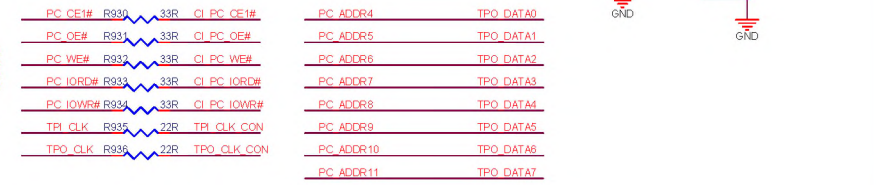
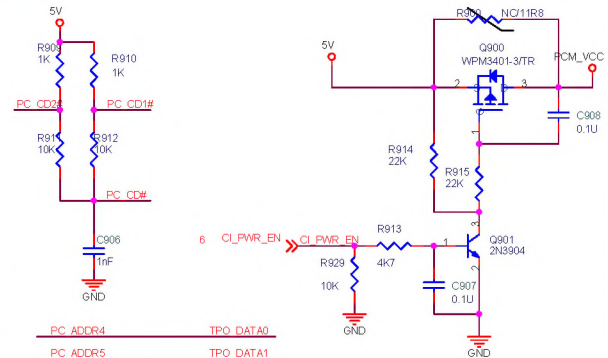
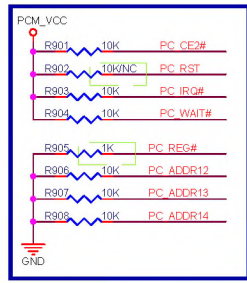
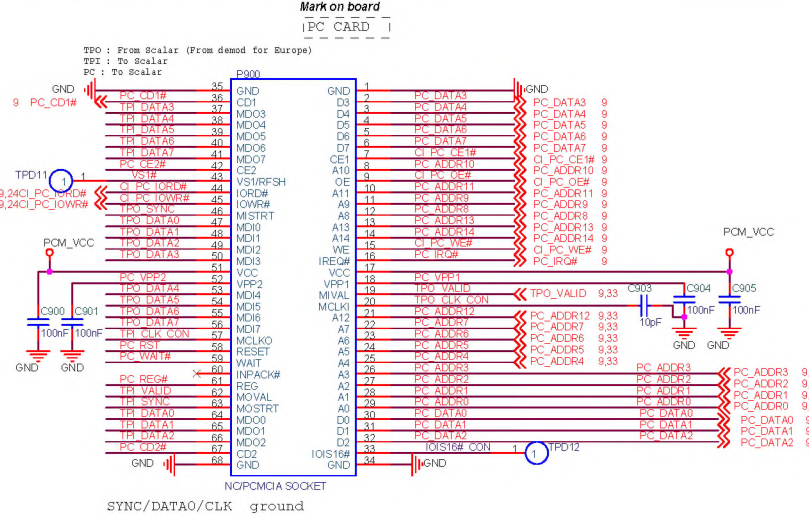
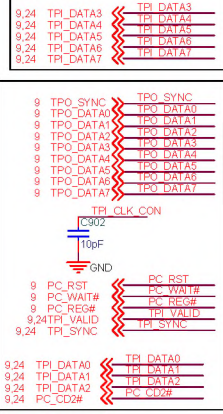




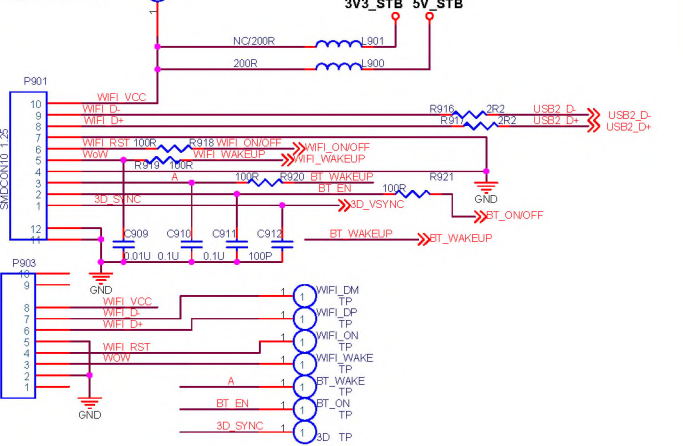
MAIN POWER IN



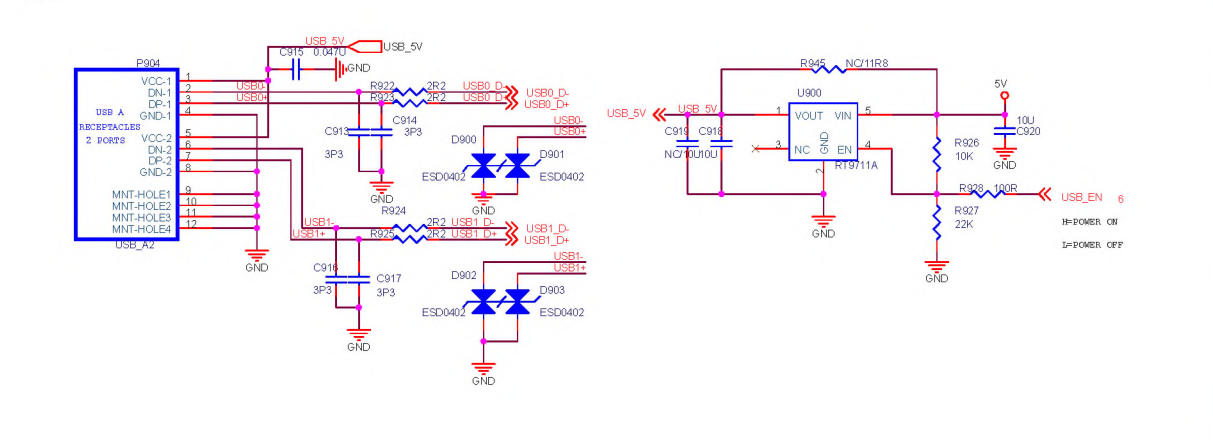
PCMCIA

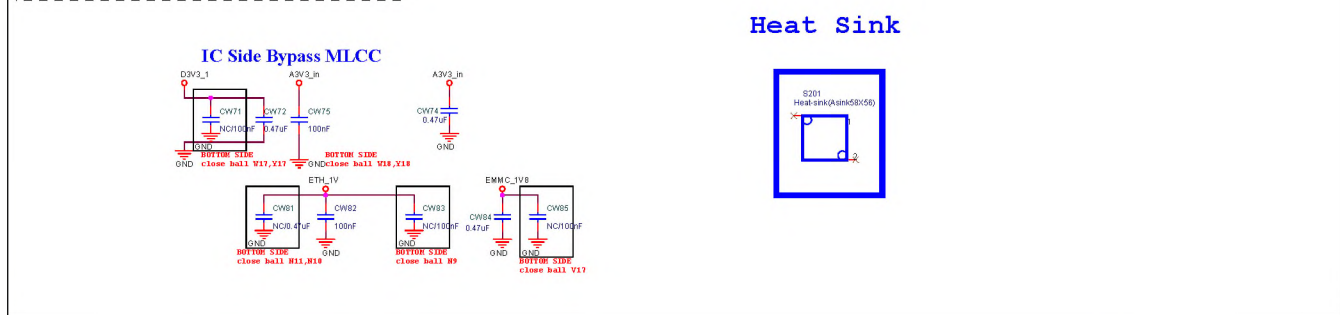
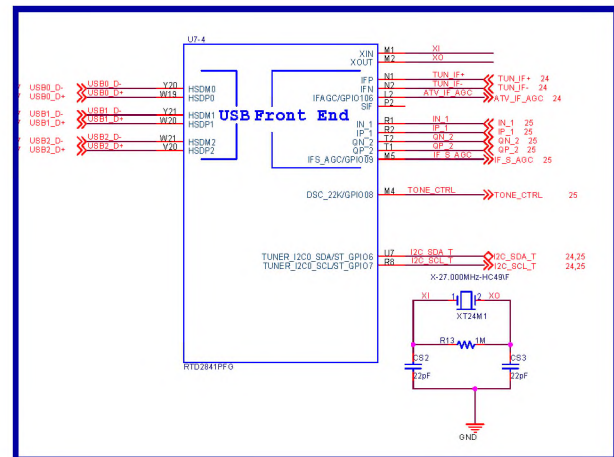
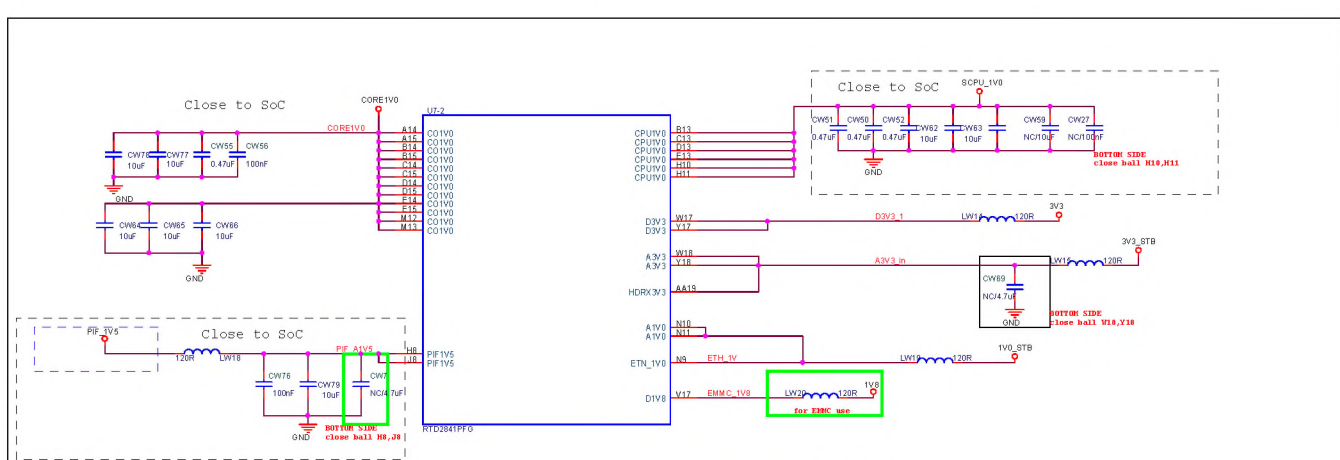
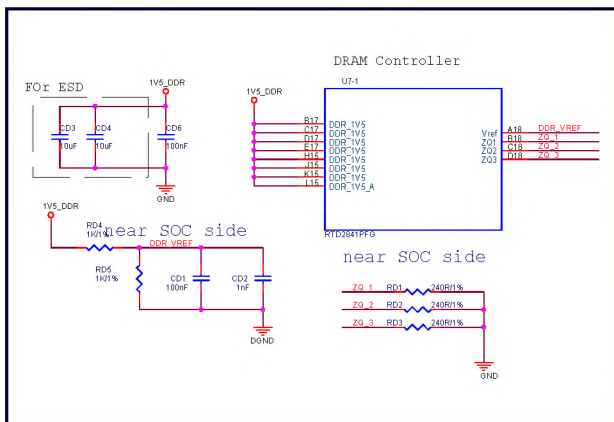


WIFI&BT

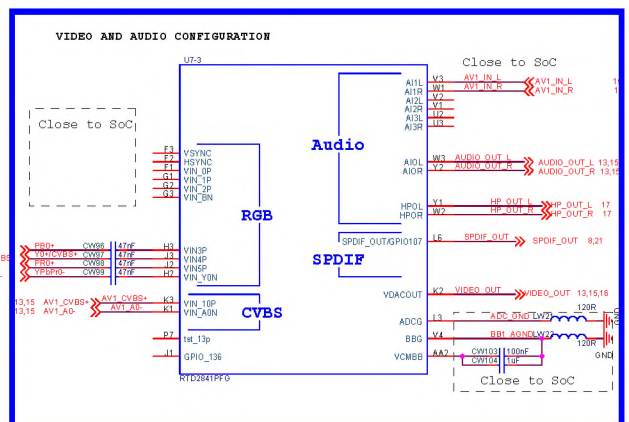
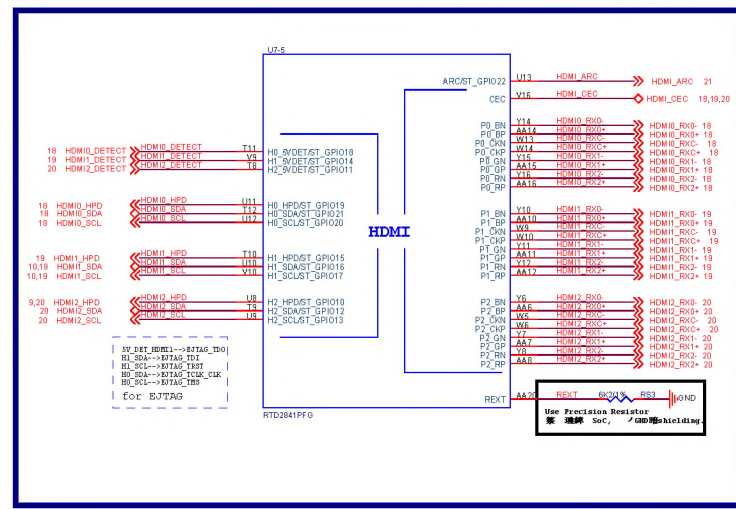
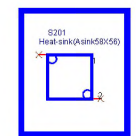


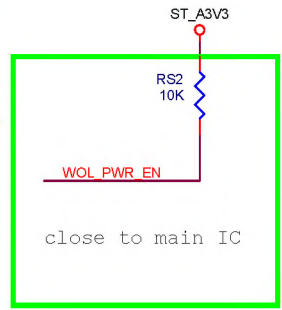
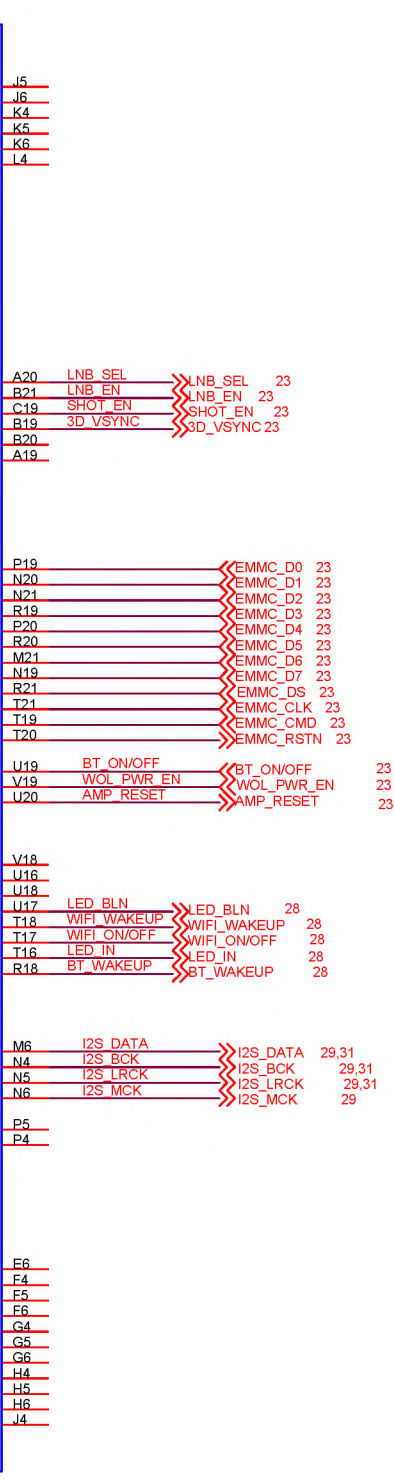
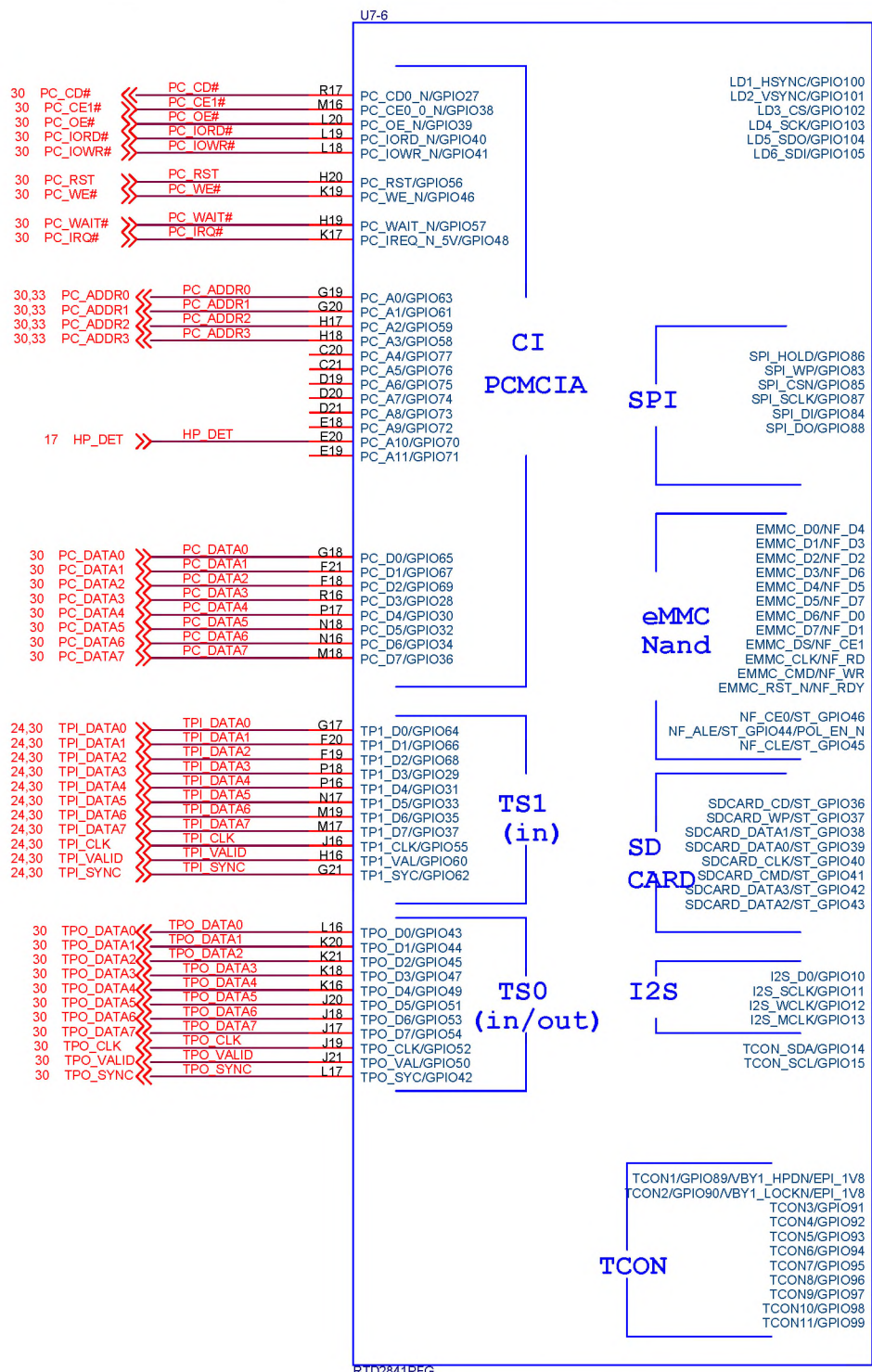
USB



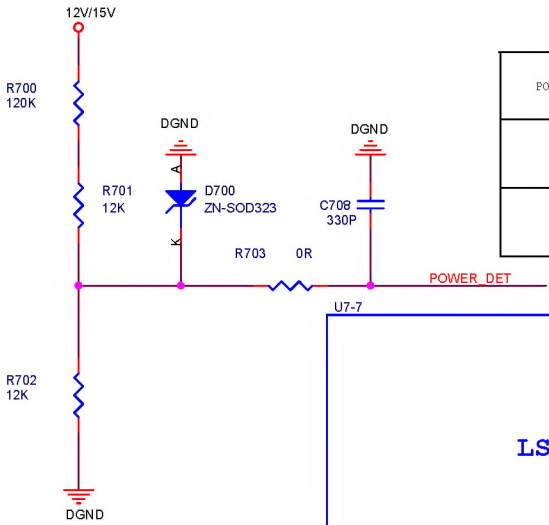


Heat Sink

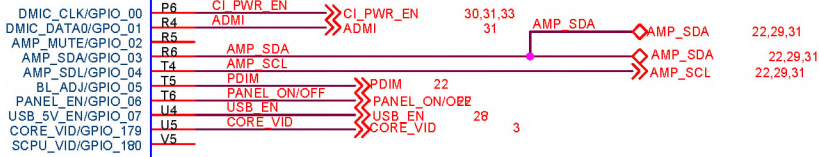
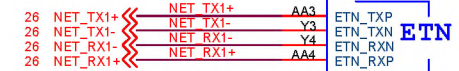
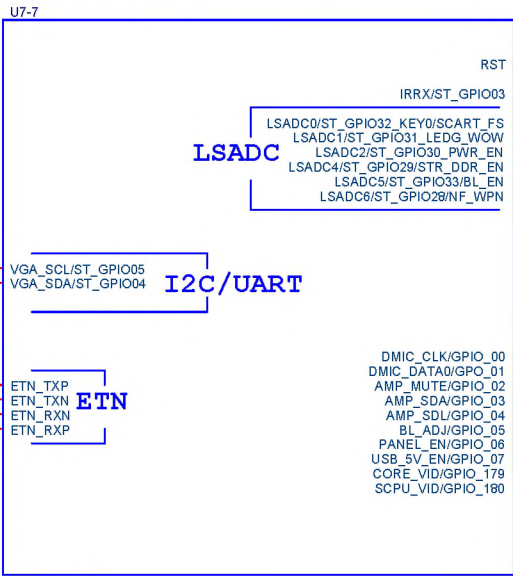




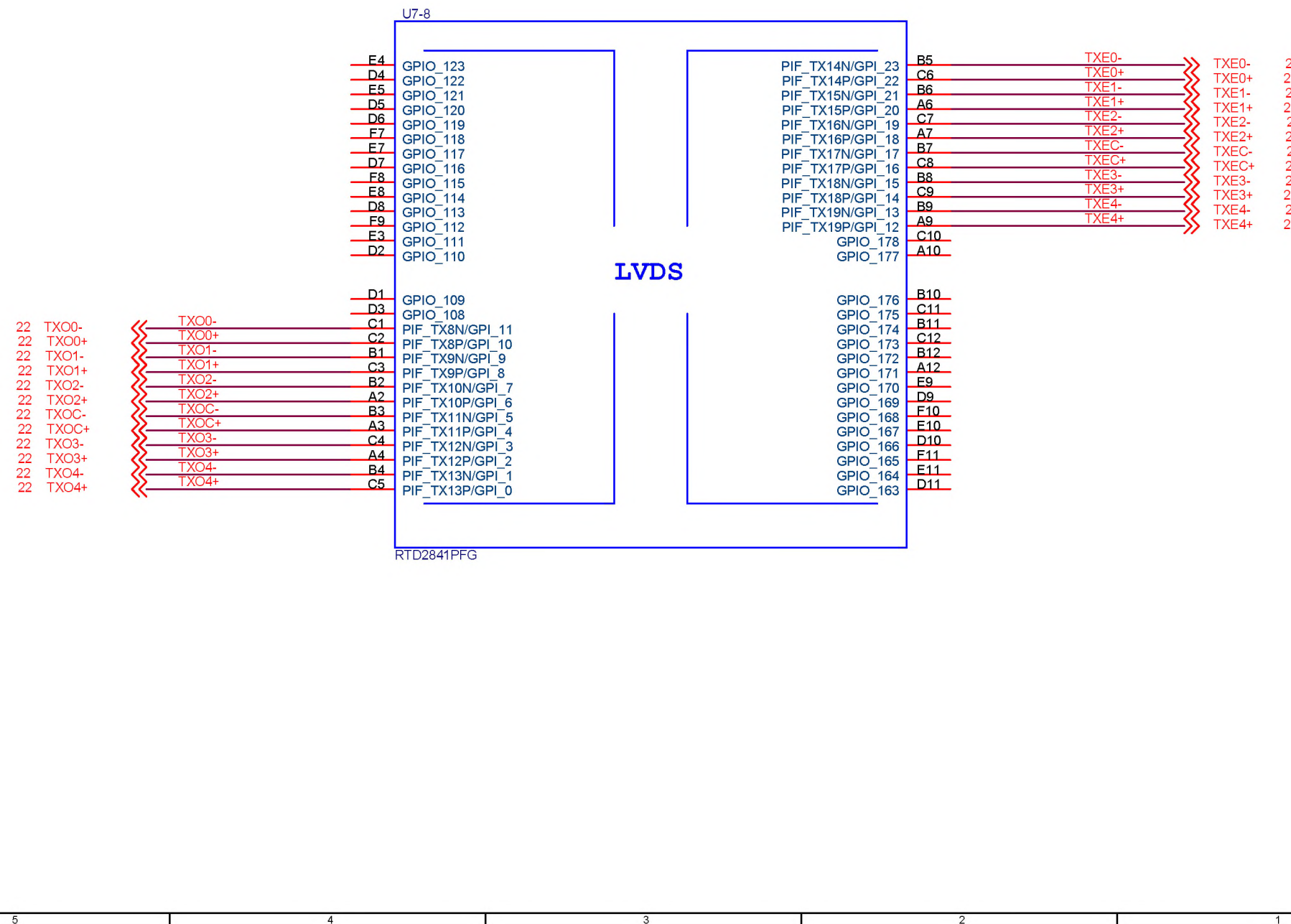
RTD2841PFG

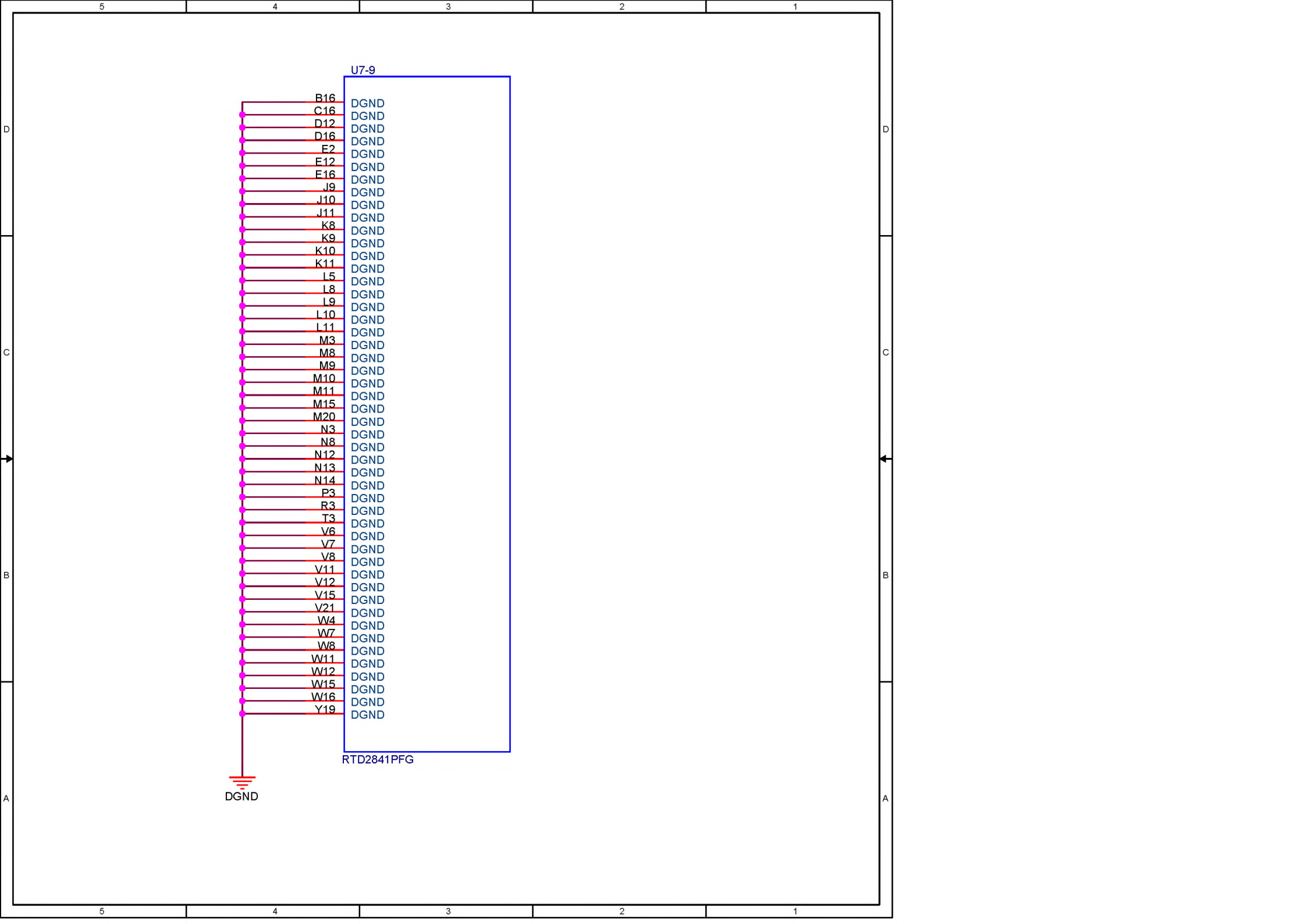


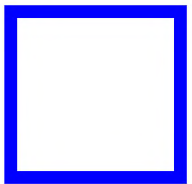
POWER INPUT	R700	R701	R702
12V	120K	12K	12K
15V	200K	12K	15K



RTD2841PFG







THGBMAG7A2JBAIR_NC

A1	NC_1
A2	NC_2
A8	NC_3
A9	NC_4
A10	NC_5
A11	NC_6
A12	NC_7
A13	NC_8
A14	NC_9
B1	NC_10
B7	NC_11
B8	NC_12
B9	NC_13
B10	NC_14
B11	NC_15
B12	NC_16
B13	NC_17
B14	NC_18
C1	NC_19
C3	NC_20
C5	NC_21
C7	NC_22
C8	NC_23
C9	NC_24
C10	NC_25
C11	NC_26
C12	NC_27
C13	NC_28
C14	NC_29
D1	NC_30
D2	NC_31
D3	NC_32
D4	NC_33
D12	NC_34
D13	NC_35
D14	NC_36
E1	NC_37
E2	NC_38
E3	NC_39
E12	NC_40
E13	NC_41
E14	NC_42
F1	NC_43
F2	NC_44
F3	NC_45
F12	NC_46
F13	NC_47
F14	NC_48
G1	NC_49
G2	NC_50
G12	NC_51
G13	NC_52
G14	NC_53
H1	NC_54
H2	NC_55
H3	NC_56
H12	NC_57
H13	NC_58
H14	NC_59
J1	NC_60
J2	NC_61
J3	NC_62
J12	NC_63
J13	NC_64
J14	NC_65
K1	NC_66
K2	NC_67
K3	NC_68
K12	NC_69

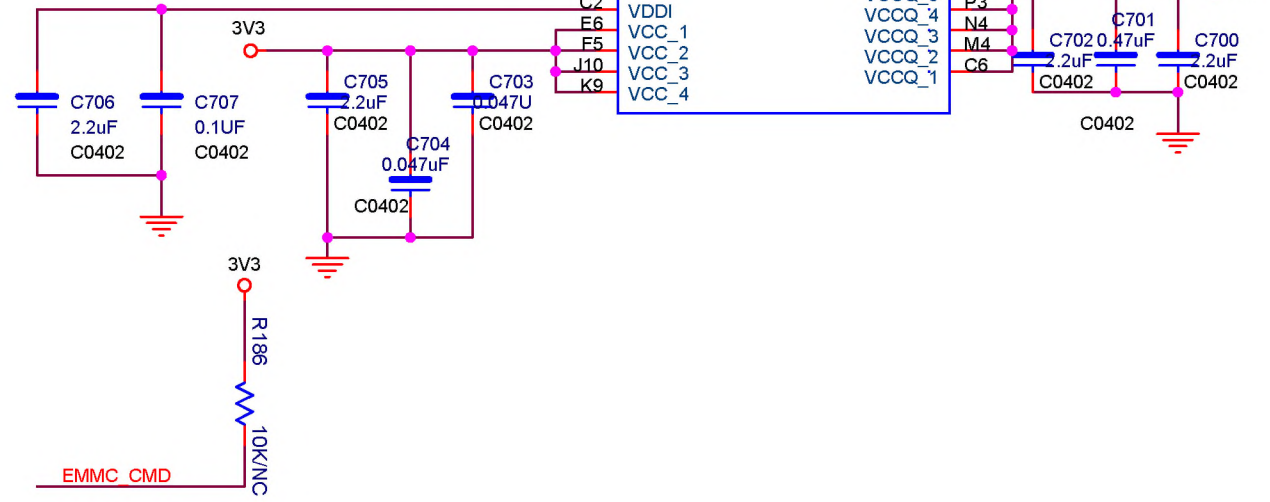
RFU/NC_16	P10
RFU/NC_15	P7
RFU/NC_14	K10
RFU/NC_13	K7
RFU/NC_12	K6
RFU/NC_11	J5
RFU/NC_10	H5
RFU/NC_9	G10
RFU/NC_8	G3
RFU/NC_7	F10
RFU/NC_6	E10
RFU/NC_5	E9
RFU/NC_4	E8
RFU/NC_3	E5
RFU/NC_2	A7
RFU/NC_1	A6

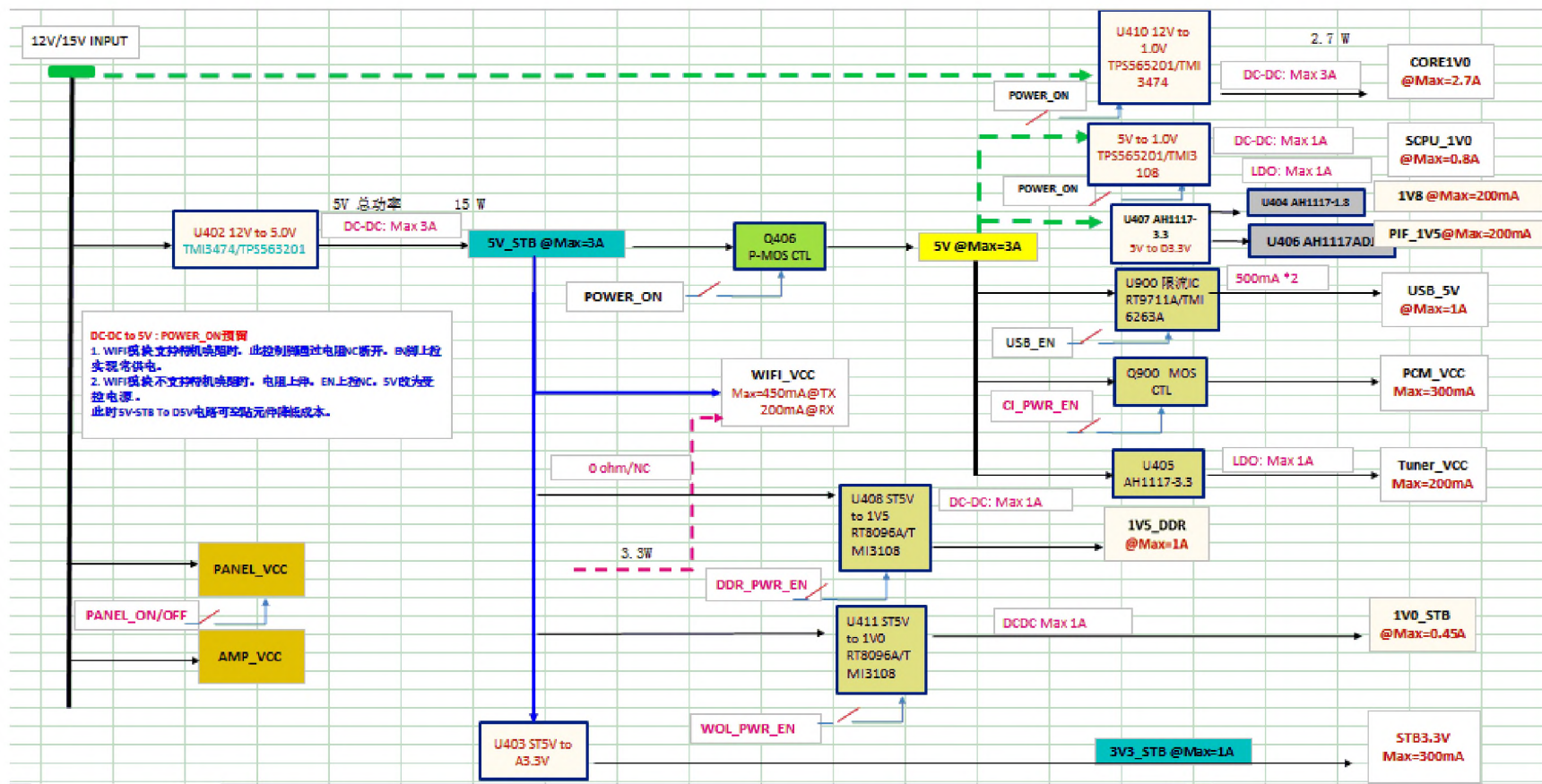
NC_107	P14
NC_106	P13
NC_105	P12
NC_104	P11
NC_103	P9
NC_102	P8
NC_101	P2
NC_100	P1
NC_99	N14
NC_98	N13
NC_97	N12
NC_96	N11
NC_95	N10
NC_94	N9
NC_93	N8
NC_92	N7
NC_91	N6
NC_90	N3
NC_89	N1
NC_88	M14
NC_87	M13
NC_86	M12
NC_85	M11
NC_84	M10
NC_83	M9
NC_82	M8
NC_81	M7
NC_80	M3
NC_79	M2
NC_78	M1
NC_77	L14
NC_76	L13
NC_75	L12
NC_74	L3
NC_73	L2
NC_72	L1
NC_71	K14
NC_70	K13

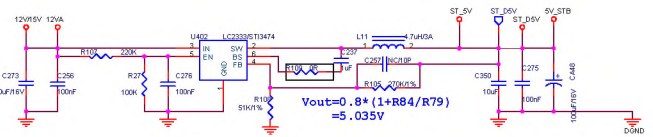
UF02-BTHGBMAG7A2JBAIR

EMMC_CLK	EMMC CLK	M6	CLK
EMMC_CMD	EMMC CMD	M5	CMD
EMMC_RSTN	EMMC RSTN	K5	RSTN
EMMC_D0	EMMC D0	A3	DAT0
EMMC_D1	EMMC D1	A4	DAT1
EMMC_D2	EMMC D2	A5	DAT2
EMMC_D3	EMMC D3	B2	DAT3
EMMC_D4	EMMC D4	B3	DAT4
EMMC_D5	EMMC D5	B4	DAT5
EMMC_D6	EMMC D6	B5	DAT6
EMMC_D7	EMMC D7	B6	DAT7

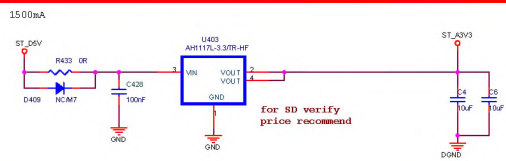
VSS_4	K8
VSS_3	H10
VSS_2	G5
VSS_1	E7
VSSQ_5	P6
VSSQ_4	P4
VSSQ_3	N5
VSSQ_2	N2
VSSQ_1	C4
VCCQ_5	P5
VCCQ_4	P3
VCCQ_3	N4
VCCQ_2	M4
VCCQ_1	C6



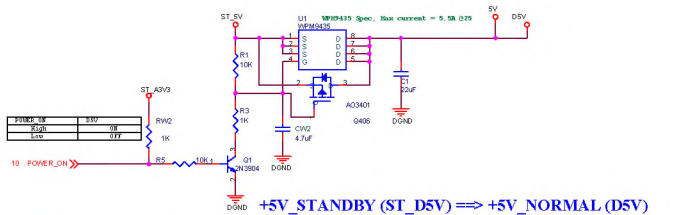




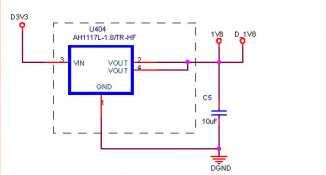
+12V Power for Standby (ST_D5V)



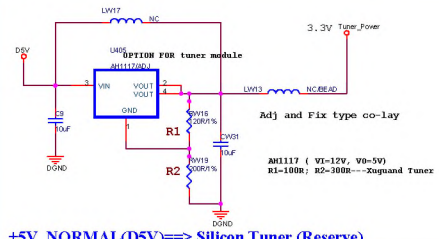
+5V_STANDBY (ST_D5V) => +3.3V_STANDBY (ST_A3V3)



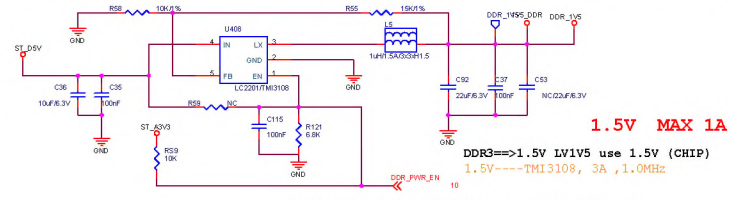
+5V_STANDBY (ST_D5V) => +5V_NORMAL (D5V)



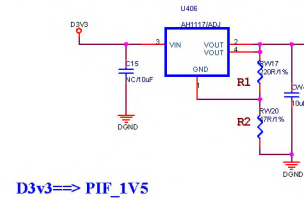
D3V3 => EMMC (DIV8)



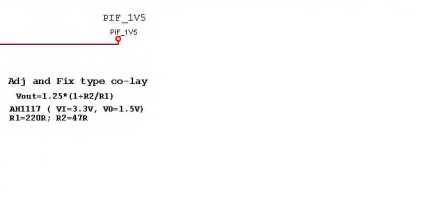
+5V_NORMAL (D5V) => Silicon Tuner (Reserve)



+5V_STANDBY (ST_D5V) => +1.5V (DDR3)

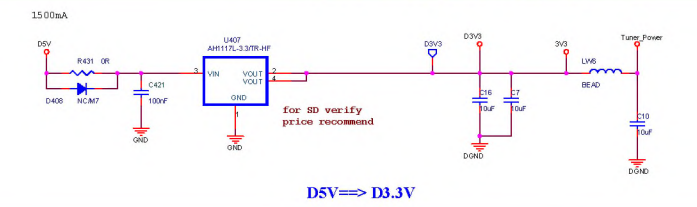


D3V3 => PIF_1V5

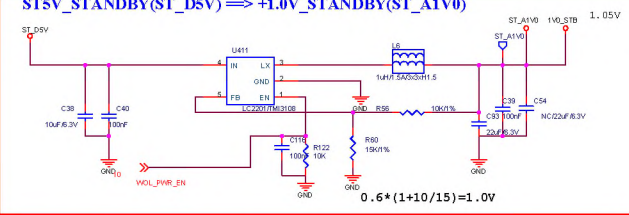


Make on board

ST_A1V0

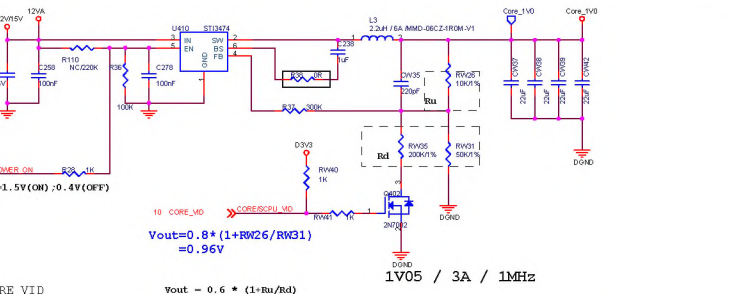


D5V => D3.3V



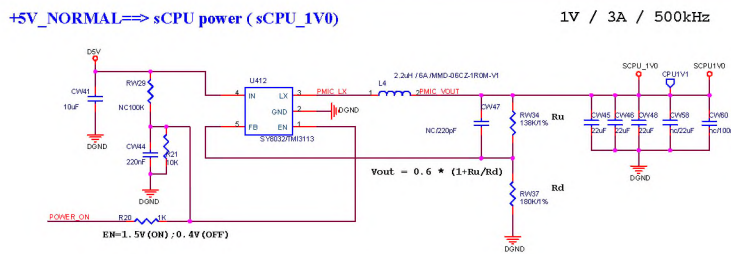
ST15V_STANDBY (ST_D5V) => +1.0V (ST_A1V0)

$0.6 * (1 + 10/15) = 1.0V$



12V => Core power (DIV0)

CORE_VID	$V_{out} = 0.6 * (1 + R_{u26}/R_{u27})$
L	$R_{u26} = 10k, R_{u27} = 50k, V_{out} = 0.96V$
H (Default)	$R_{u26} = 10k, R_{u27} = 200k, V_{out} = 1V$

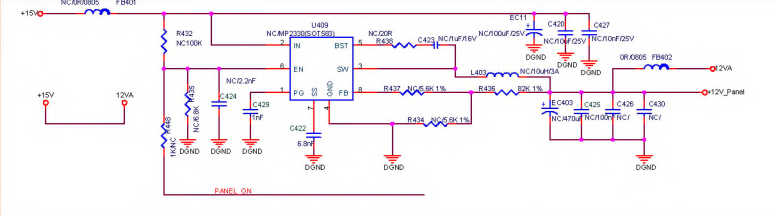
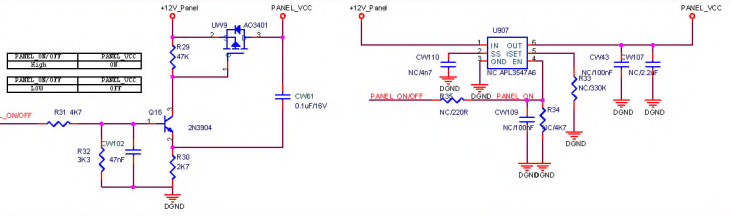
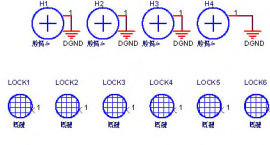


+5V_NORMAL => sCPU power (sCPU_V10)

1V / 3A / 500kHz

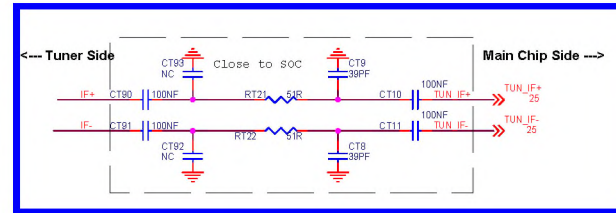
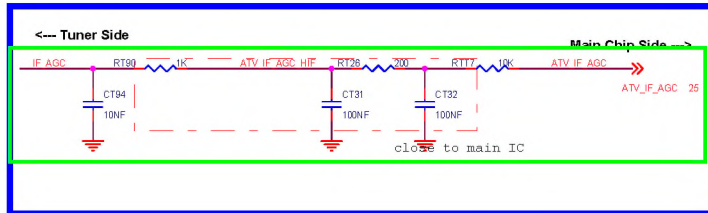
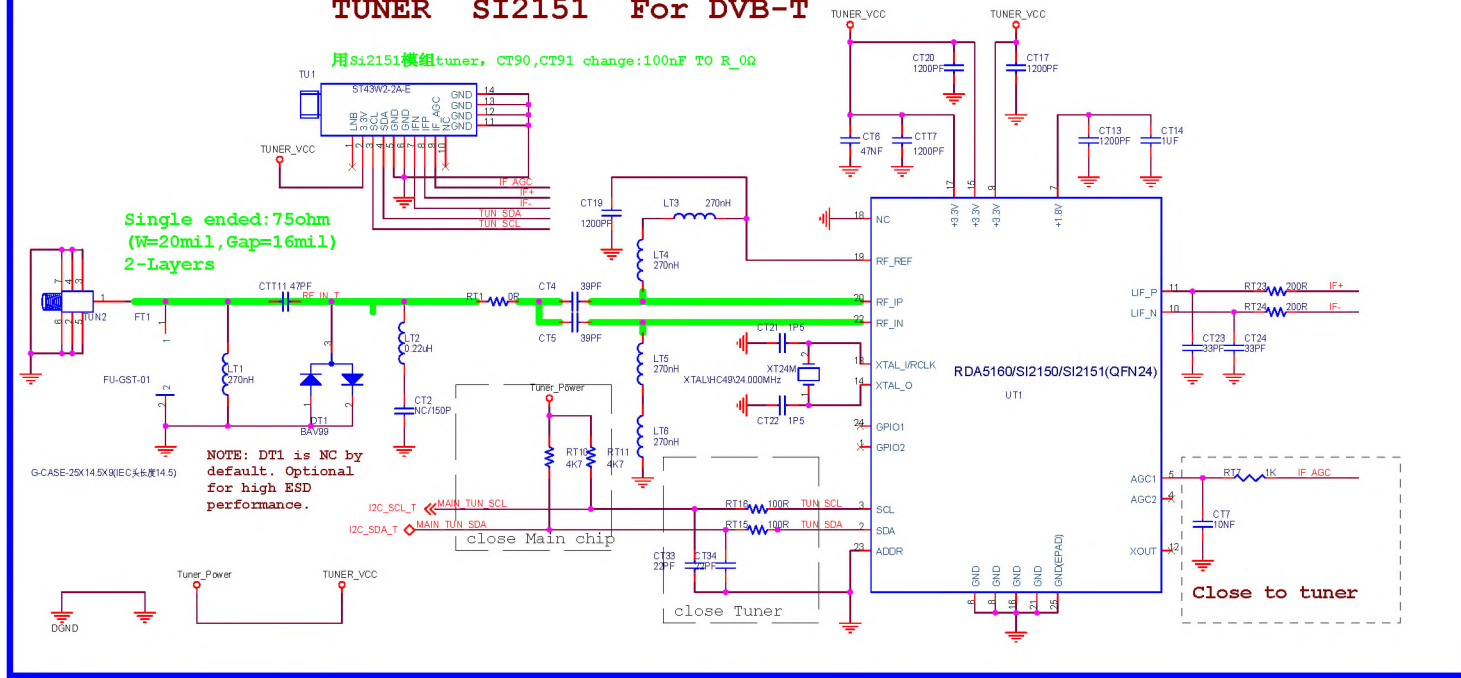
$V_{out} = 0.6 * (1 + R_{u26}/R_{u27})$

$R_{u26} = 10k, R_{u27} = 100k, V_{out} = 1.06V$
--

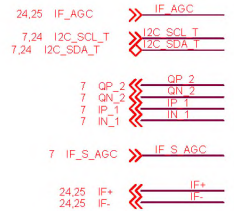


TUNER SI2151 For DVB-T

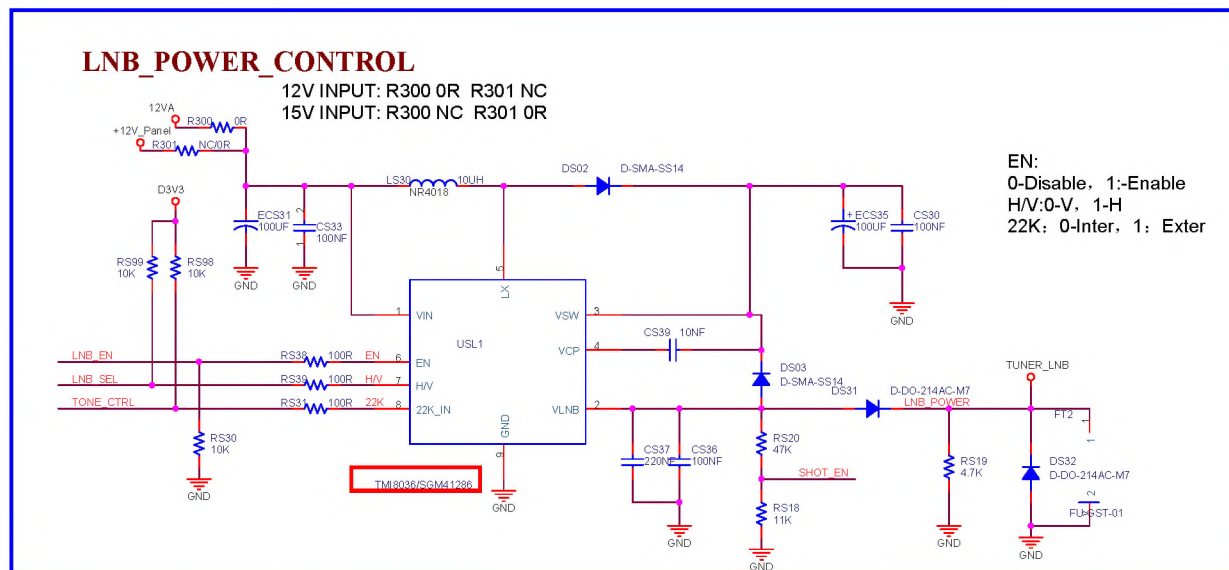
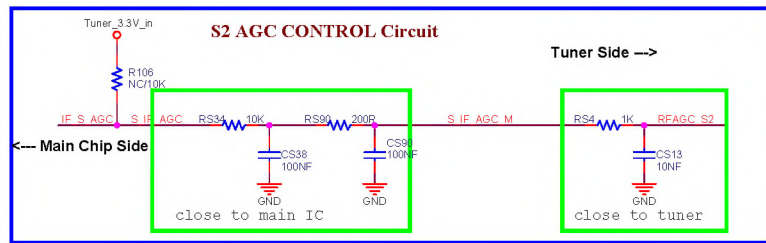
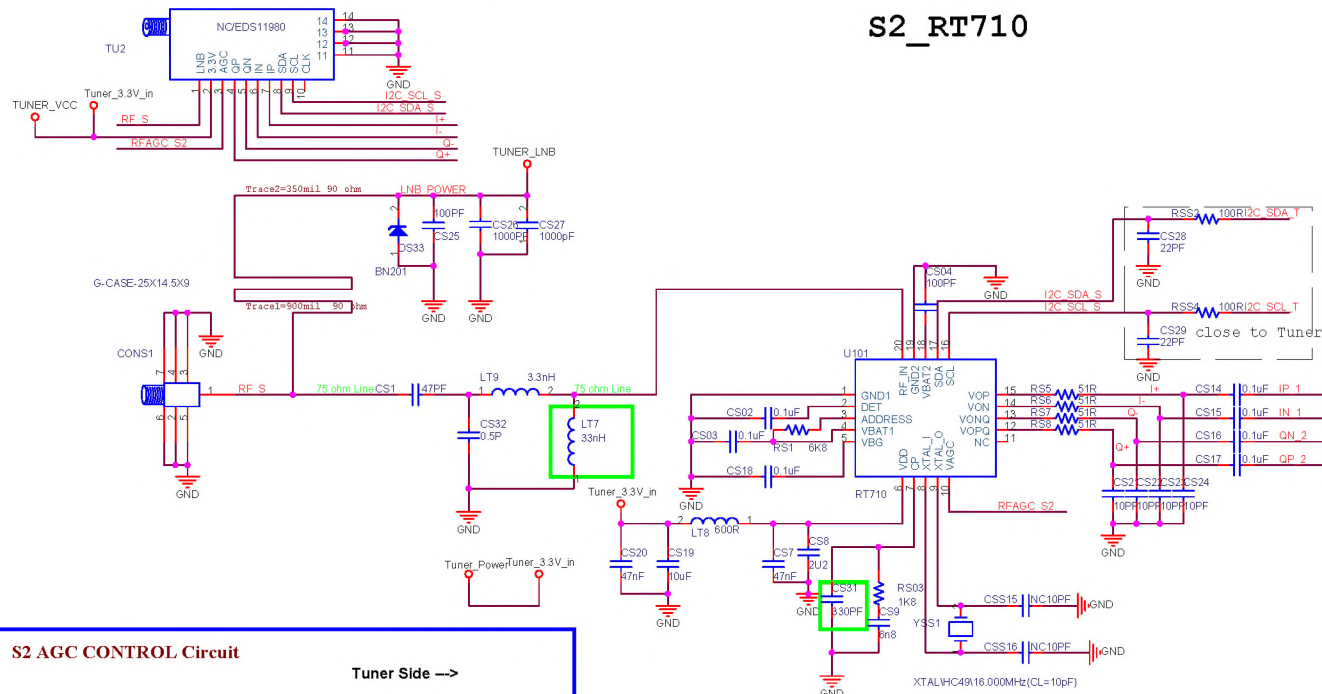
用Si2151模组tuner, CT90,CT91 change:100nF TO R_0Q

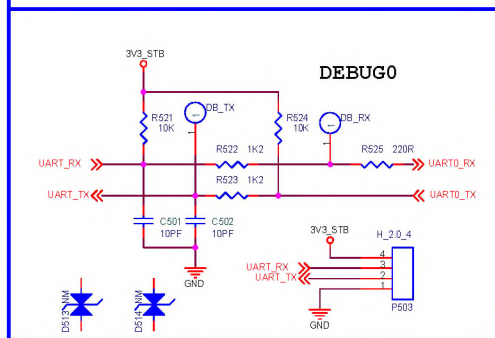
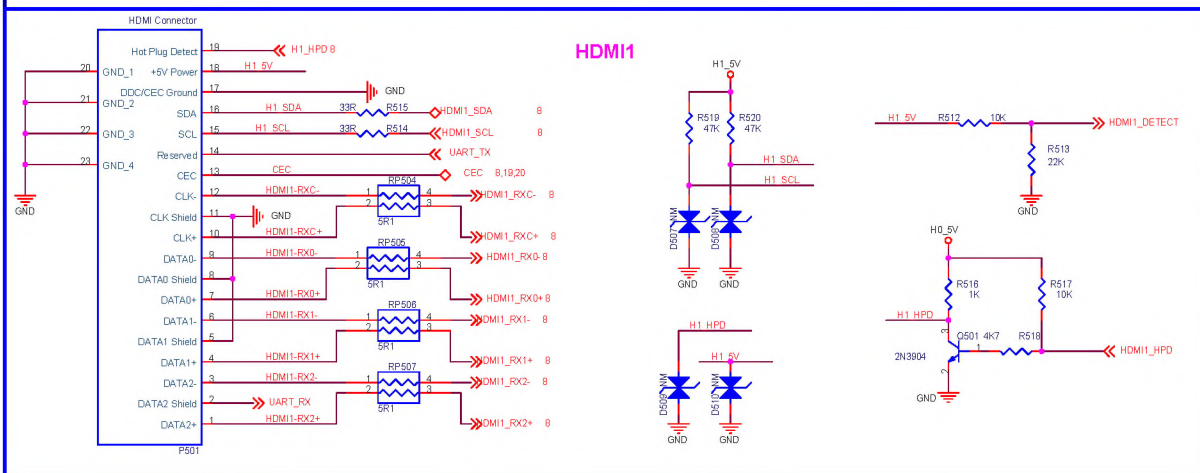
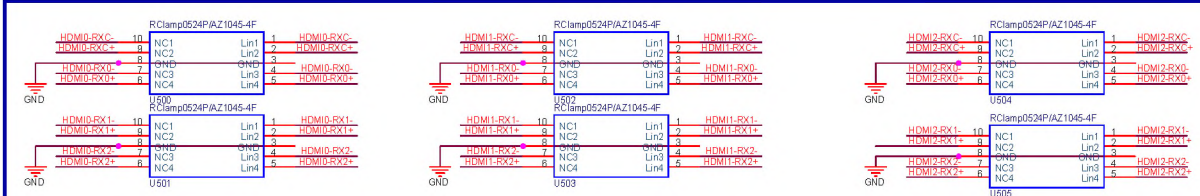
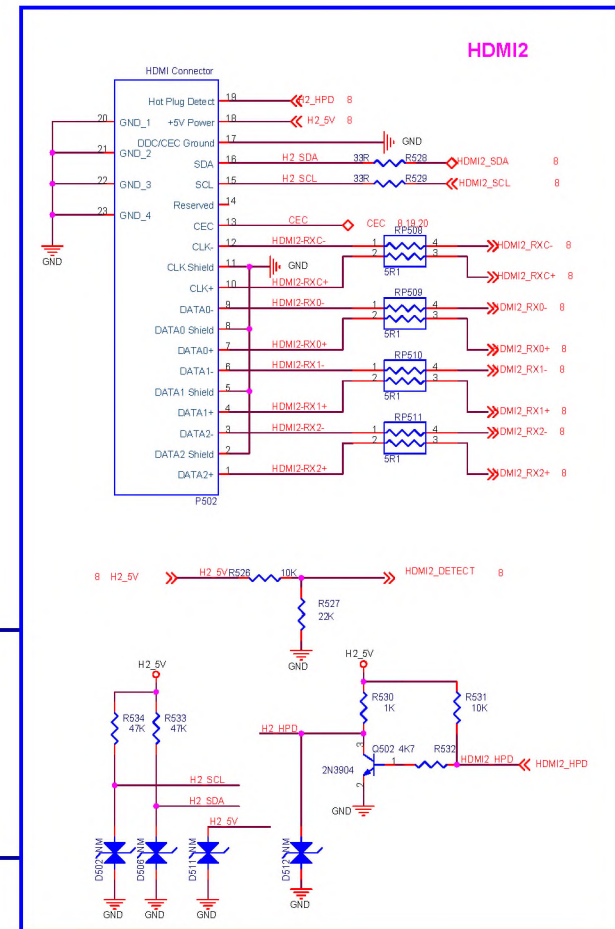
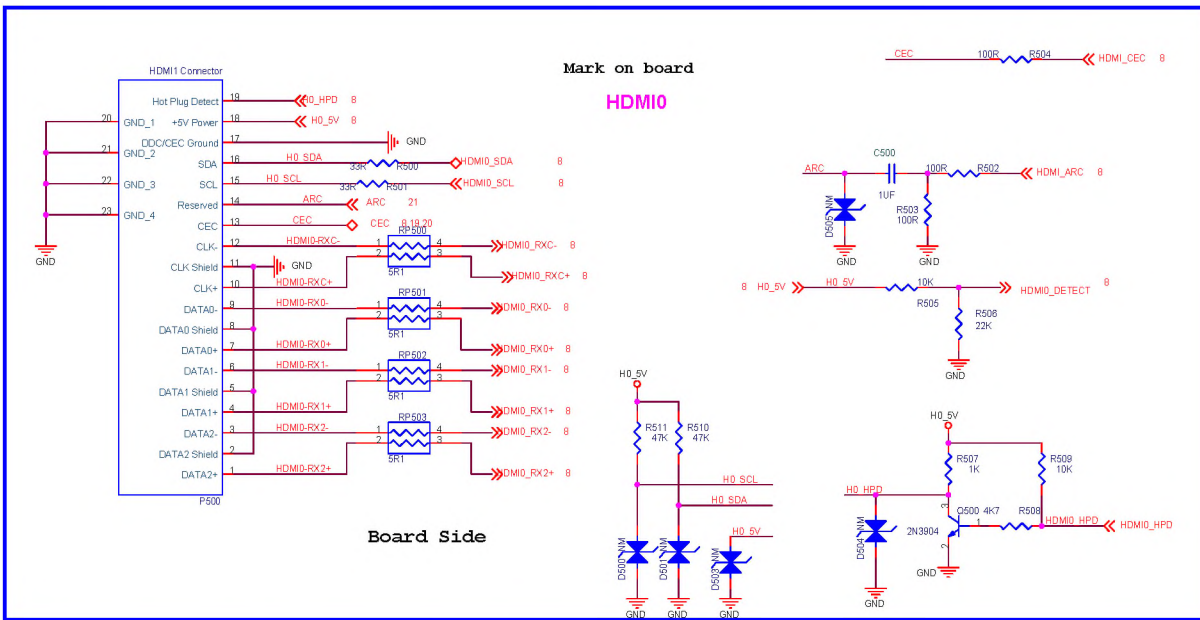


Main Chip <--

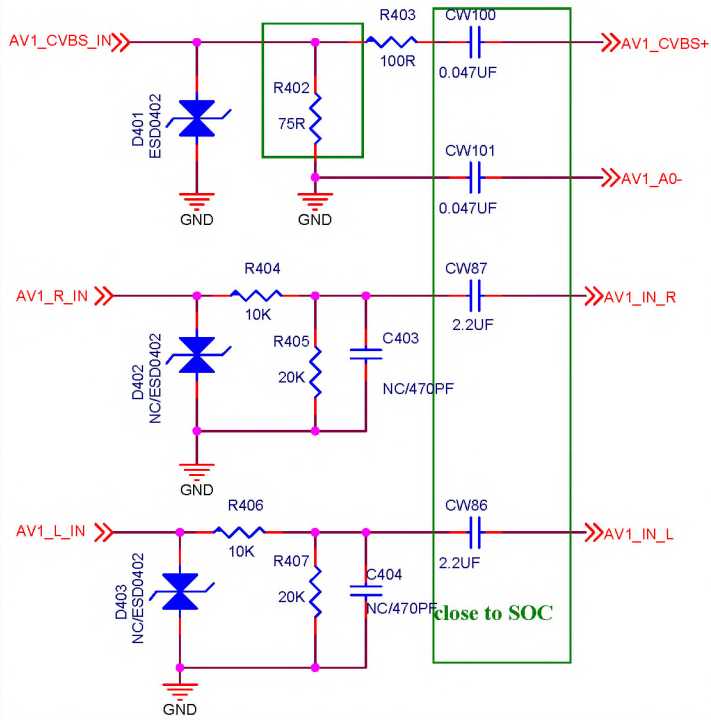
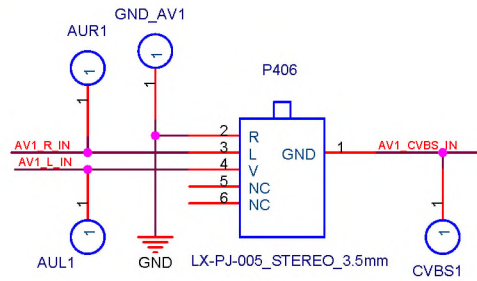


S2_RT710

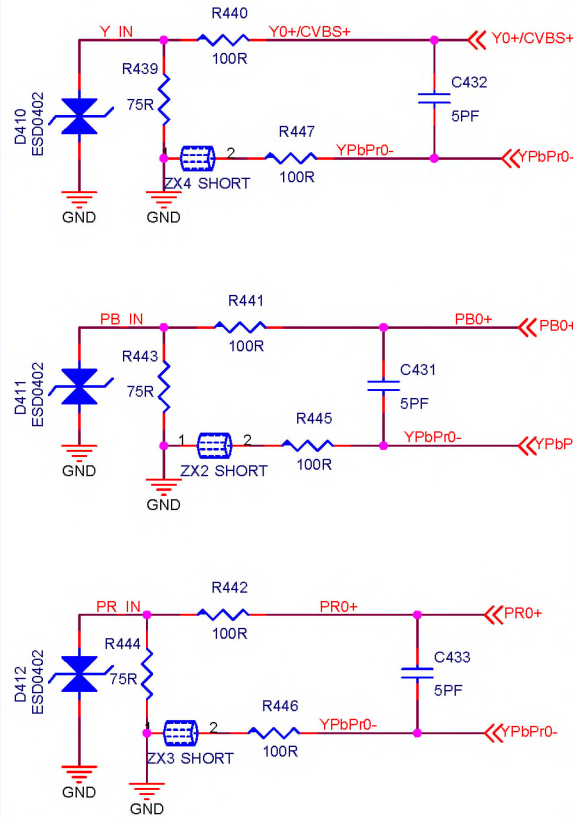
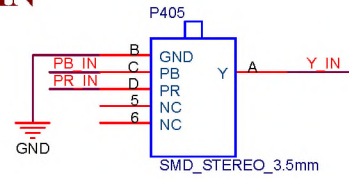




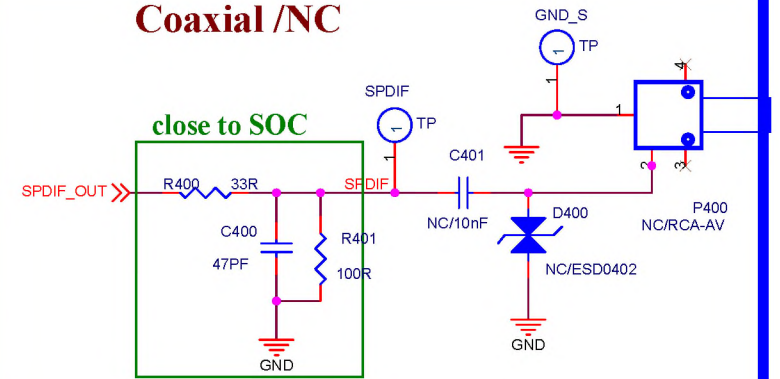
AV IN



YUV IN

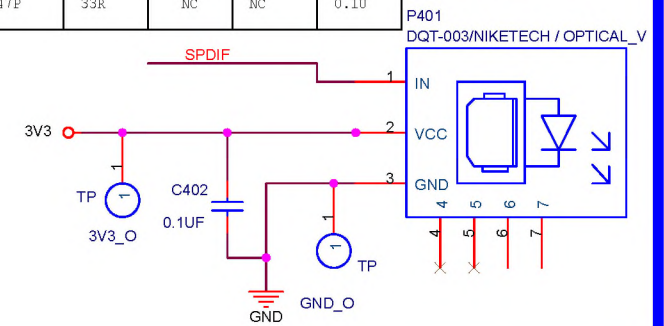


Coaxial / NC



	C400	R400	C401	R401	C402
coaxial	220P	300R	0.1U	100R	NC
optical	47P	33R	NC	NC	0.1U

Optical



5

4

3

2

1

